

Method for realizing LUT circuit and LUT circuit thereof

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Abstract of **CN1434603**

This invention discloses a realization method for LUT circuit including designing a memory as the look-up circuit memory, a status device for LUT circuit capable of accessing the memory; storing in the said memory the source and target address in sequence of the source and related relations between the source and target dresses. When a source needs to get its target address by data, the LUT status device will LUT the source address in the memory to find out the said data target address, reducing capacity of the LUT memory to the minimum, mostly put in IC or ASIC chips.

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